



501.36694CV4

JPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: S. SHUKURI, et al.

Application. No. 10/635,511

Filed: August 7, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING
DEPOSITED LAYER FOR GATE INSULATION

Group: 2814

Examiner: Long Pham

Confirm. No.: 8446

AMENDMENT

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 3, 2005

Sir:

In response to the Office Action mailed March 3, 2005, please amend the
above-identified application as listed in the following, and as set forth on the
following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.